

Figure 1 – Predistortion PA circuit

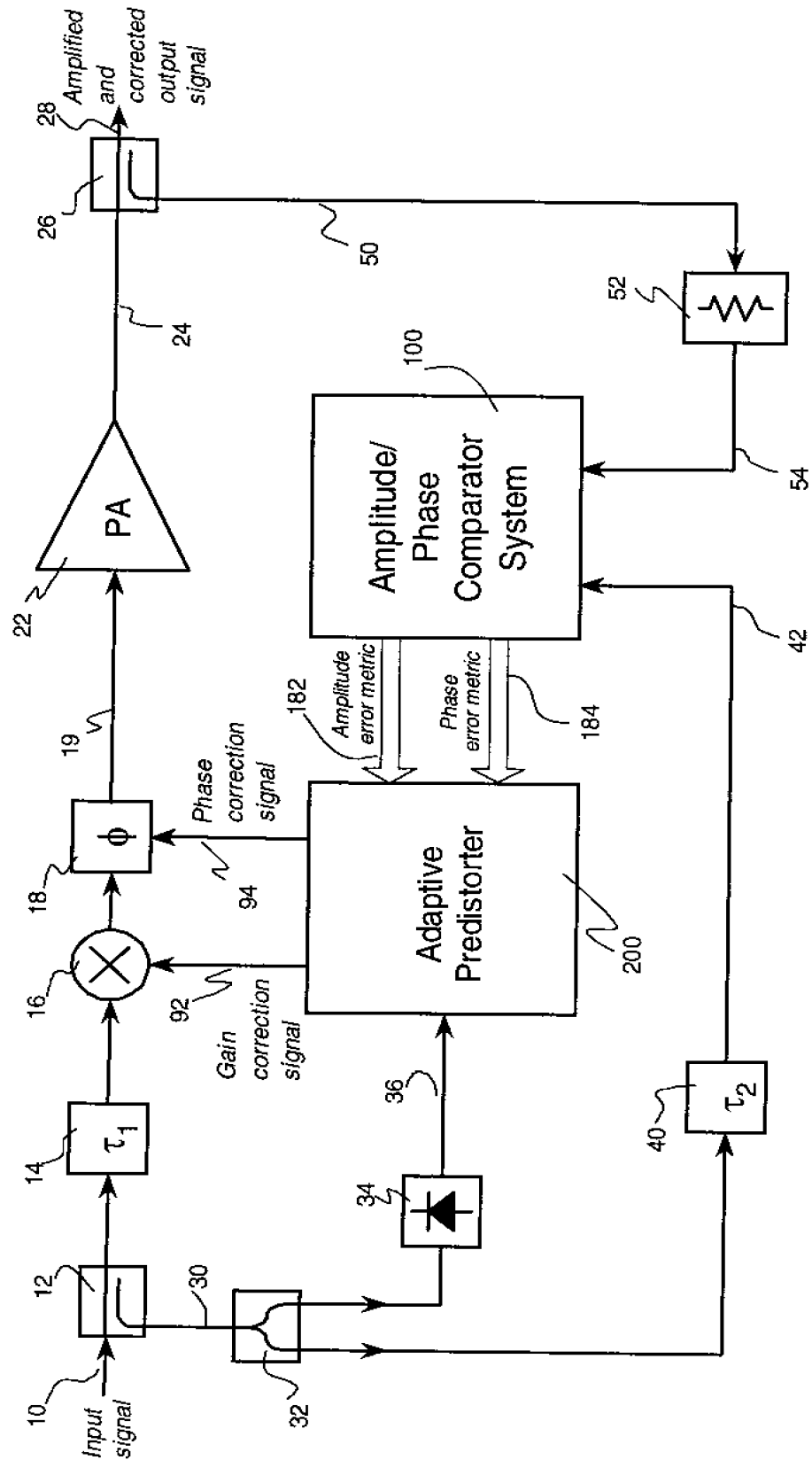


Figure 2 – Amplitude/Phase Comparator circuit (first embodiment)

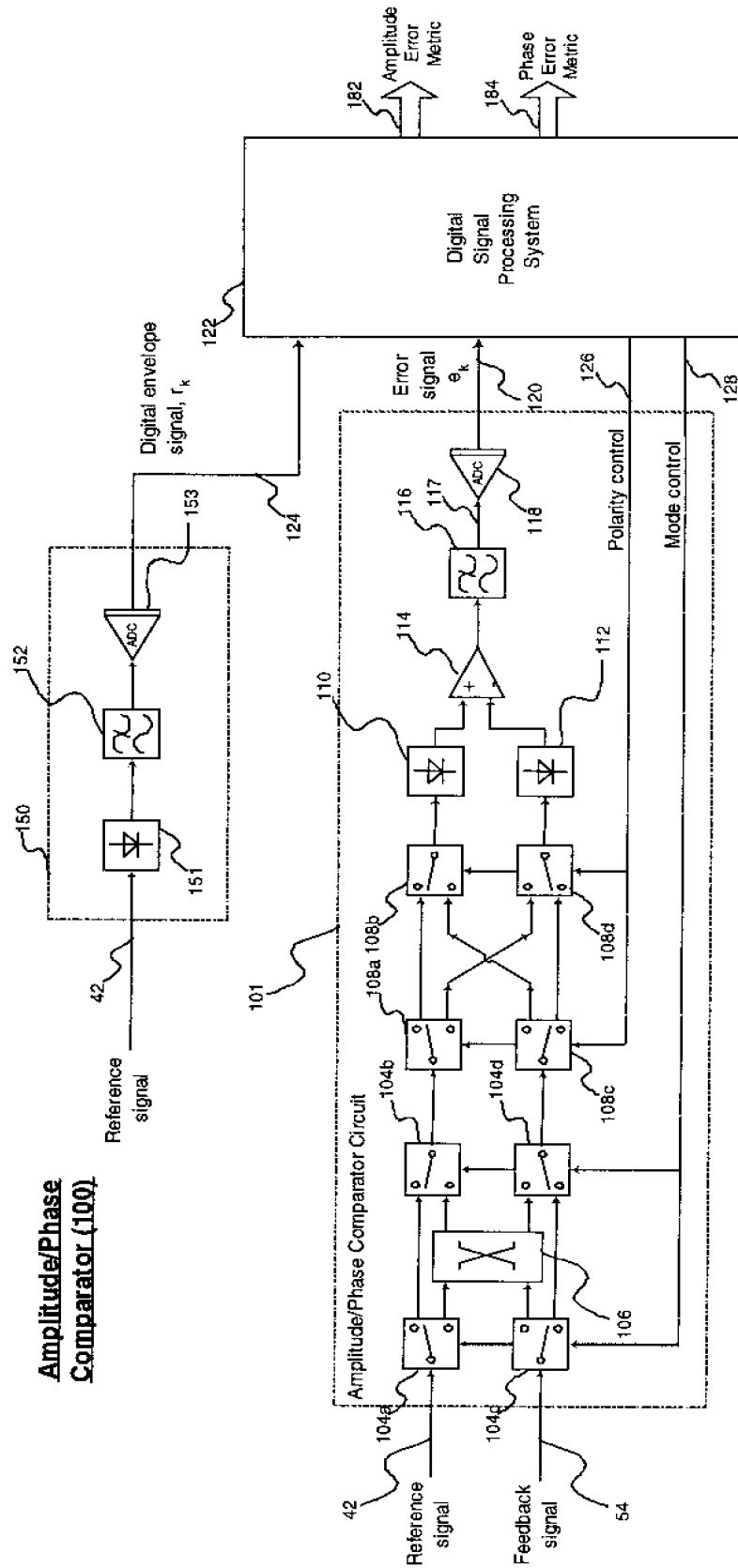


Figure 3 – Amplitude/Phase Comparator circuit (second embodiment)

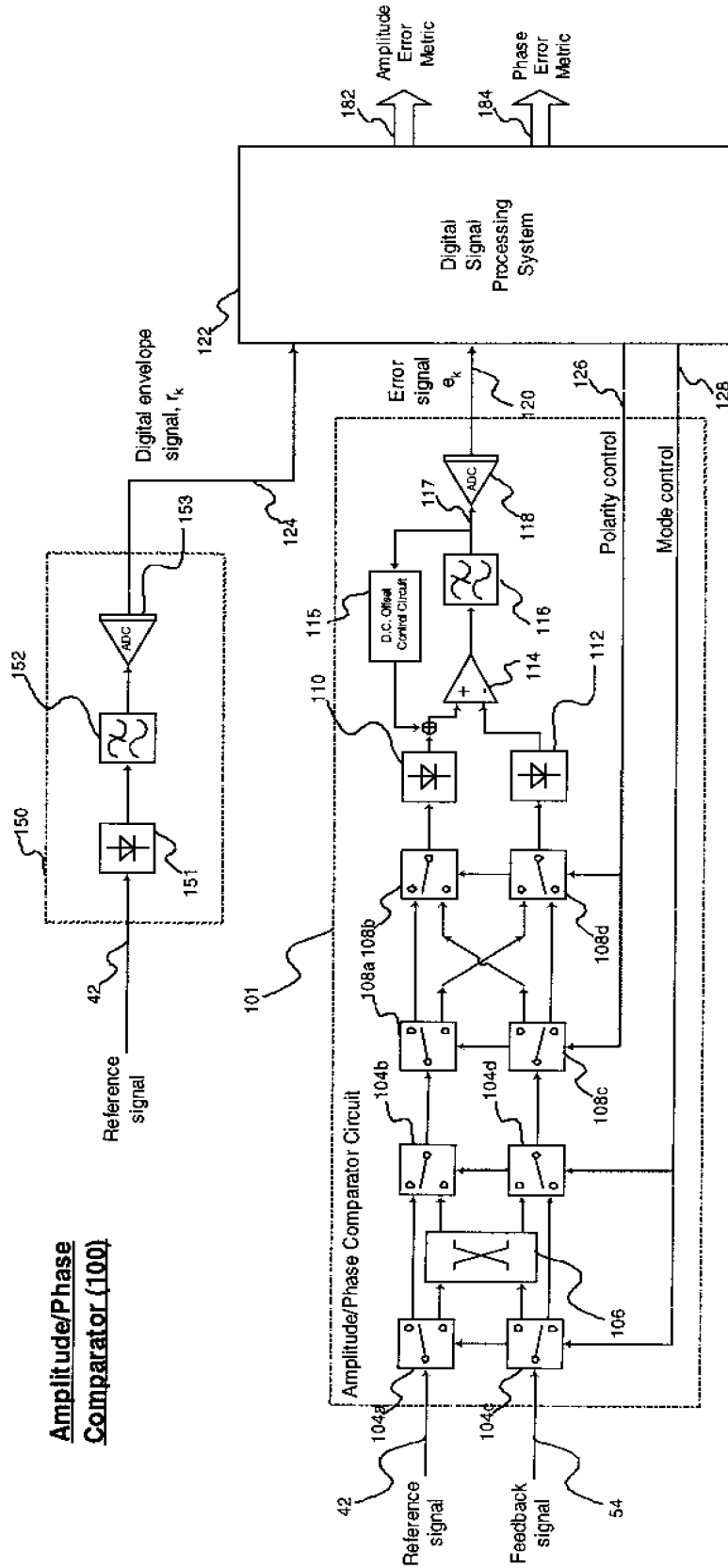


Figure 4 – Predistortion circuit for use with the present invention

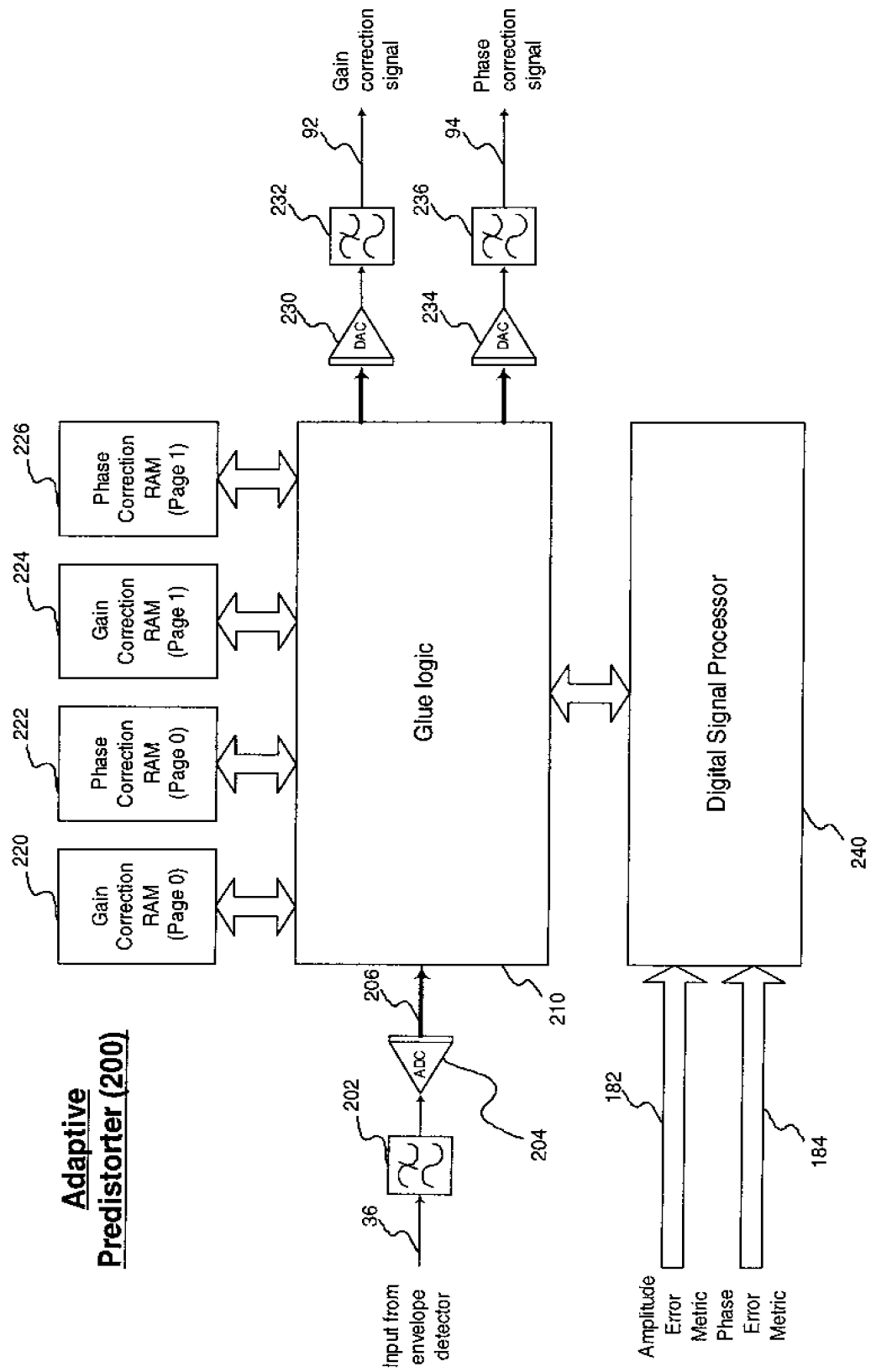


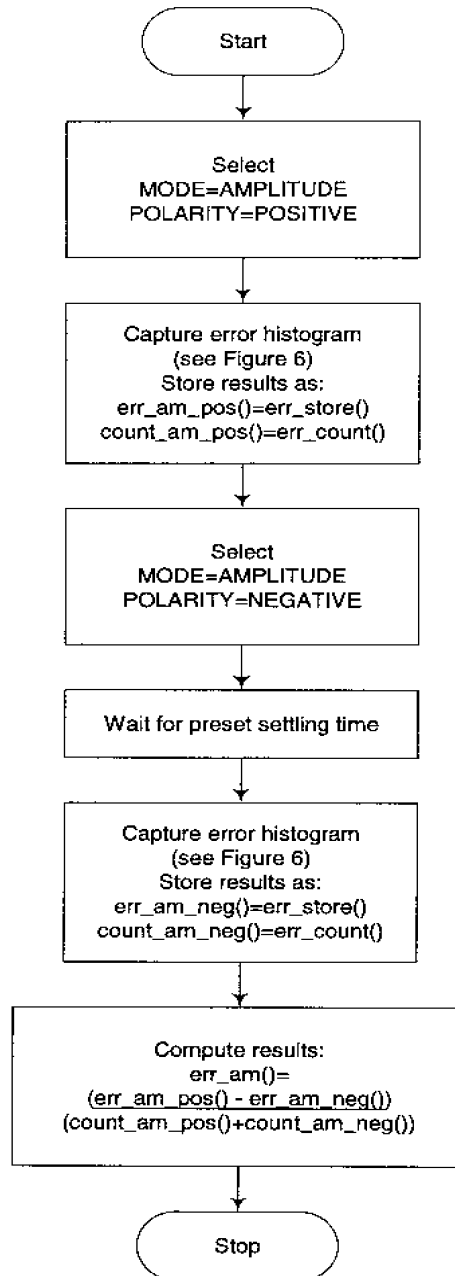
Figure 5 – Magnitude error computation

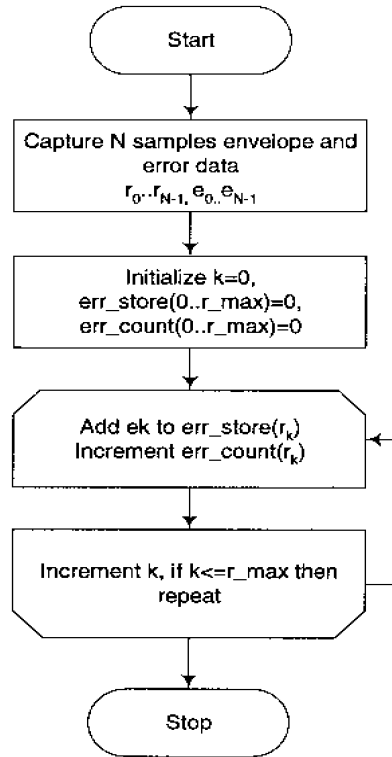
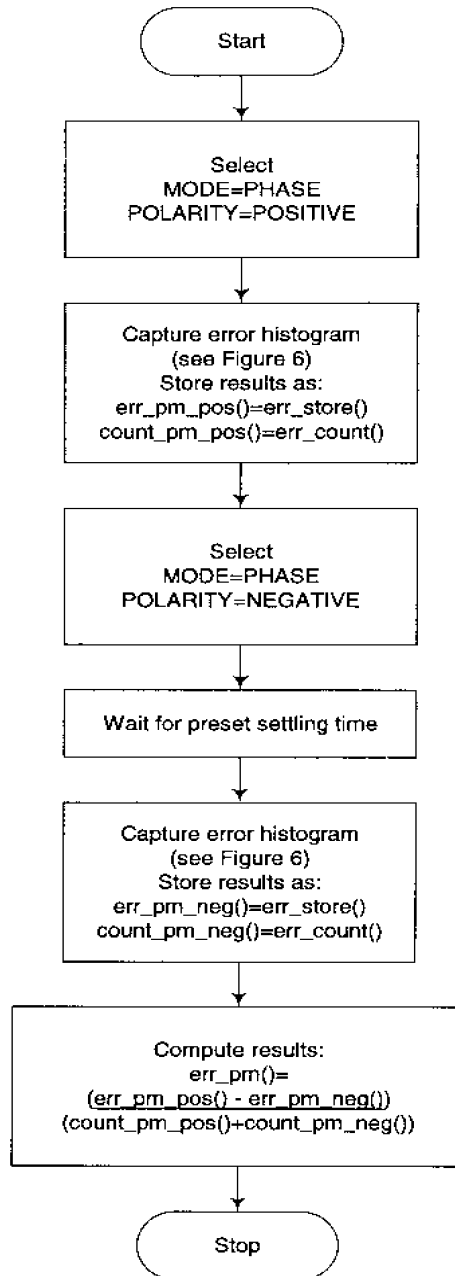
Figure 6 – Histogram capture computation

Figure 7 – Phase error computation**END OF DOCUMENT**